

SYSTEM AND METHOD FOR EXITING FROM AN INTERRUPT MODE IN A MULTIPLE
PROCESSOR SYSTEM

ABSTRACT OF THE DISCLOSURE

5 A system and method for interrupt processing includes a technique for exiting from
interrupt mode in multiple processor systems. Those processors that were in a suspended or halt
state immediately before entering the interrupt mode are released immediately with reference to the
resolution of the interrupt condition. Those processors not responsible for the processing tasks
associated with resolving the interrupt condition serially exit from interrupt mode on a time-delayed
10 basis following the resolution of the interrupt condition.